

WHAT IS CLAIMED IS:

1. A method for driving an LCD in which gate lines are sequentially scanned in 1 vertical period, the method
5 comprising the steps of:

sequentially generating a plurality of gate pulse voltages having 1st to 3rd levels while being synchronized with vertical clock signal in said 1 vertical period;

in invert driving, dividing the generating period of the
10 plural gate pulse voltages into a charge period, a holding period and a discharge period in respective polar periods corresponding to the 1st to 3rd levels of the plural gate pulse voltage; and

15 converging pixel voltage of the discharge period to a common voltage level,

wherein the 3rd level exists in a range between the 1st level and the 2nd level.

2. A method for driving an LCD as claimed in claim 1,
20 wherein the rising time of the 3rd level of the liquid crystal is smaller than the falling time of the 3rd level of the liquid crystal.

3. A method for driving an LCD as claimed in claim 1,

wherein the holding period is set as more than 2 horizontal periods.